

AMENDMENT UNDER 37 C.F.R. § 1.116
U. S. Application No. 09/472,869

REMARKS

Claims 1-9 and 11 are pending in the application.

In reply to the Response filed February 10, 2004, the Examiner maintained the previous claim rejection. The status of the claims is the following.

Claims 1-4 are allowed.

Claims 6-9 are objected to as being dependent from a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 5 and 11 are rejected under 35 U.S.C. § 103(a) as being unpatentable over previously-cited Sokawa et al. (US 6,353,460, hereinafter "Sokawa").

By the present Amendment, Applicant seeks to rewrite claim 6 in independent form including the limitations of claim 5 and cancel claim 5. This amendment should be entered after the final rejection, since the limitations of claim 5 have already been examined and should place claims 6-9 in form for allowance.

With regard to claim 11, Applicant submits that claim 11 is not simply a method claim which corresponds to the limitations of claim 5, and Sokawa fails to teach or suggest all of the limitations of claim 11. In particular, claim 11 recites that the step of selecting the clock frequency comprises: outputting a control signal from a controller, said control signal depending upon the frame rate which is detected; receiving said control signal into a selector, said selector connected to outputs of a plurality of phase locked loops, wherein each phase locked loop has a

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predetermined clock frequency, and selecting one predetermined clock frequency of one of said plurality of phase locked loops based upon the control signal received by the selector.

More specifically, Applicant submits that Sokawa does not disclose a selector connected to outputs of a plurality of phase locked loops, wherein each phase locked loop has a predetermined clock frequency, and selecting one predetermined clock frequency of one of the plurality of phase locked loops based upon the control signal received by the selector. The Examiner has not even asserted that the reference discloses this feature of the claim. Sokawa discloses only a single phase locked loop circuit. See col. 18, lines 31-34. Moreover, Sokawa does not disclose selecting one predetermined clock frequency of one of the plurality of phase locked loops based upon the control signal received by the selector. Therefore, claim 11 is allowable over Sokawa.

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

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The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



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Date: May 24, 2004